

Notice of Allowability

Application No.

09/872,212

Examiner

Guy J. Lamarre

Applicant(s)

KEBICHI ET AL.

Art Unit

2133

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 8/22/05 and 11/14/2005.
2. ☒ The allowed claim(s) is/are 1-30, 32, and 34-37 now renumbered 1-35.
3. ☒ The drawings filed on 01 October 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 11/14/2005.
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____



Guy J. Lamarre, P.E.
Primary Examiner
11/14/2005

Examiner's Amendment

1. This office action is in response to Applicants' Amendment of 22 Aug. 2005.
- 1.1 Claims 1-30, 32, and 34-37 remain pending.
- 1.2 The rejections of record are withdrawn in response to Applicants' Amendment.

Examiner's Amendment

2.0 An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Justin D. Wagner on 14 Nov. 2005.

The following has been amended:

- 1) **Claim 13** is replaced by:

A circuit for testing multiple memories, comprising:
at least one BIST controller within an integrated circuit, wherein the BIST
controller has a synchronization output which is activated when the controller is in an
idle state;
a resume input on the integrated circuit coupled to the at least one BIST
controller, wherein the resume input comprises a single input pin to the integrated
circuit; and
automated test equipment (ATE), wherein the ATE receives the synchronization
output through a single channel between the ATE and the integrated
circuit.

2) **Claim 19** is replaced by:

A method of testing memory, comprising:
providing one or more BIST controllers in an integrated circuit, wherein at least
one of the BIST controllers is associated with a group of one or more memories, and
wherein the integrated circuit has a resume input coupled in parallel to the one or more
BIST controllers, the resume input comprising a single input pin to the integrated circuit;
independently performing a first phase of a memory test on the group of
memories associated with the BIST controller; and
upon completion of the first phase of the memory test, asserting a
synchronization signal indicating that the group of memories are at a
common synchronization state, wherein the synchronization signal is
asserted through a single channel to automated test equipment (ATE).

Conclusion

* Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

or faxed to: (571) 273-8300 for all formal communications.

Hand-delivered responses should be brought to Customer Services, 220 20th Street S.,
Crystal Plaza II, Lobby, Room 1B03, Arlington, VA 22202.

Any inquiry concerning this communication or earlier communications from the
examiner should be directed to Guy J. Lamarre, P.E., whose telephone number is (571) 272-
3826. The examiner can normally be reached on Monday to Friday from 9:30 AM to 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's
supervisor, Albert De Cady, can be reached at (571) 272-3819.

Any inquiry of a general nature or relating to the status of this application or proceeding
should be directed to the Group receptionist whose telephone number is (571) 272-3609.

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Information regarding the status of an application may also be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Guy J. Lamarre, P.E
Primary Examiner
11/14/2005
